Apple][Advance Order Information	
APPLE-II	
ADVANCE ORDER INFORMATION	
Apple Computer, Inc. 770 Welch Road, Palo Alto, CA 94304 (415) 326-4248	
2 April 1977	

Apple][Computer Information

Page 0001 of 0004

APPLE-II

Apple Computer shipped its first computer in March 1976. Since that time we have grown to become a major supplier of personal computing systems.

APPLE-II is our newest product. It represents several advancements in the personal computing state-of-the-art: the incorporation of color graphics onto a single-board system along with color graphic commands in a BASIC; a ROM BASIC and monitor; and the use of the new $16k \times 1$ RAM chips to achieve 48k bytes on-board memory space.

Because of the advanced design, APPLE-II uses 1/3 to 1/4 the number of components previously used in 1st generation personal computing products. This has a significant effect on reliability and enables APPLE-II to be priced below competitive systems of inferior capability. As all APPLE products, APPLE-II is delivered **fully assembled & tested**, making it a truly off-the-shelf computer system, priced below the cost of many kits.

THE PRODUCT

Apple-II consists of a 6502 microprocessor, powerful video display electronics including color graphics, from 4k to 48k bytes on-board RAM, up to 12k bytes ROM (8k supplied—6k BASIC, 2k monitor), a cassette interface, an ASCII keyboard port and the APPLE GAME I/O connector for paddles and other game controls, all on a 8½ × 11 (legal size) PC Board! Listed below is a brief overview of the product.

VIDEO DISPLAY

The Apple-II includes a versatile video display section which displays memory as **text**, **color graphics**, or **hi-resolution graphics** (completely transparent memory access). Both graphics modes can be optionally selected to include 4 lines of text at the bottom of the display area. **All display modes are software selectable**. In addition, the user can select under software one of two memory blocks to be displayed.

Text

- 40 characters/line, 24 lines.
- 5 × 7 upper case characters.
- Normal or inverse video.
- Extensive display control software in ROM.
- · Full cursor control.
- Fast display.
- Dual page mode.

Color Graphics

- 40h × 48v resolution.
 (40h × 40v with 4 lines text).
- 16 colors.
- BASIC commands to use easily: COLOR = , PLOT, HLIN, VLIN, SCRN.
- Color generation done digitally.
- Dual page mode.

Hi Resolution Graphics

- 280h × 192v dots.
 (280h × 160v with 4 lines text).
- 4 colors—black, white, blue, green.
- Displays 8k bytes (requires 12k minimum system).
- Dual page mode.

MEMORY

APPLE-II uses **both** the 4k and the new 16k dynamic RAMS. By using 3 rows of the 16k chips (24 chips) the incredible density of **48k bytes on-board RAM** is achieved. APPLE first delivered a microcomputer using 16k RAMS in December, 1976. Apple is, at this time (1st quarter 77), the only personal computer manufacturer using these advanced chips. Since these 16k chips reduce the memory chip count by a factor of 16 (compared to 2102's), the reliability is far superior and the power requirements are reduced enormously. The 16k RAM will be the chip used in future microcomputers, available now, **only** from APPLE.

APPLE-II also includes 8k bytes of ROM memory which containes BASIC (6k) and a powerful system monitor (2k). This makes loading BASIC from tape unnecessary and assures that BASIC is just a few keystrokes away. Two extra ROM sockets are included for future APPLE software.

Up to 48K bytes on-board RAM—no external RAM boards needed!

- The only personal computer system to use 16k RAMs.
- 24 chips = 48k bytes, the equivalent of 384 2102's at 1/20 the power!
- Unique automatic dynamic RAM refresh system (completely transparent).
- BASIC and MONITOR on ROM.

I/O

APPLE-II contains a fast cassette interface (1500 bps) which can be called from the monitor or from BASIC (SAVE, LOAD). It is extremely reliable and can be used with **any** home cassette recorder.

APPLE-II has 8 connectors which serve as a peripheral card motherboard. The connectors contain the address, data, control & timing busses, all fully buffered.

Each peripheral slot is numbered (0-7). Many APPLE-II peripherals will contain on-card ROM-based driver software, enabling the BASIC to call the peripheral by simply specifying a slot number (i.e. PR #4, IN #5).

Also included are an ASCII keyboard port, speaker, apple game i-o connector. The APPLE GAME I/O connector greatly simplifies interfacing game controls. It has inputs for 4 pots (paddles, joysticks, etc.), 3 TTL inputs and 4 TTL outputs.

- Fast cassette interface: 1500 bps
- 8-slot motherboard
 - Fully buffered busses & timing.
 - Daisy-chained interrupt priority structure.
 - Daisy-chained DMA priority structure for burst-mode or cycle-steal DMA.
- ASCII keyboard input port.
 - Supplies all power.
 - 7 bits ASCII and positive strobe inputs.
- GAME I/O Connector. Inputs for Paddles, joysticks, buttons, etc.

• PERIPHERAL BOARDS

- Parallel Interface Board—April 77.
- Serial Interface Board—April 77.
- A/D, D/A Board—April 77.
- EROM Board with 2708 programmer—May 77.
- Music Synthesis Package—June 77.
- Mini Floppy Controller—July 77.

SOFTWARE

Basic

APPLE BASIC is an integer BASIC supplied in 6k bytes of ROM and includes the following features (ih addition to normal basic features):

- APPLE BASIC is a fast translated BASIC.
- Any length variable names (ALPHA, BETA\$).
- Syntax and range errors indicated immediately when line is entered.
- · Multiple statements on one line.
- Integers from -32767 to +32767.
- String arrays to 255 characters.
- Single dimension integer arrays.
- GRAPHICS COMMANDS: COLOR = expr, PLOT, HLIN (draw horizontal line), VLIN, SCRN (x, y) (reads the screen color).
- Paddle read function: PDL (0-3).
- TEXT and GRaphics commands set display mode from BASIC.
- Immediate execution of most statements.
- Memory boundary adjust commands (does not destroy current program).
- Break and Continue program execution.
- Debug commands: line number trace and variable trace (DSP APPLE).
- Switchable I/O device assignments using APPLE ROM-based peripherals.
- Direct memory access: PEEK, POKE, CALL commands.

- Cassette SAVE & LOAD commands.
- · Auto line number mode.
- · RND, SGN, ASC, LEN and ABS functions.
- POP instruction pops the return stack one level.
- · GOTO expr, GOSUB expr allowed.

Monitor

The system monitor is supplied in ROM and enables the user to enter, examine, debug and run assembly language programs. It also contains very useful screen control routines, the cassette routines, a dis-assembler and other debug aids. Since it is supplied in ROM, it is immediately available when the computer is turned on and cannot be inadvertently clobbered.

- 2k byte ROM monitor.
- Screen control (intelligent display routines).
- Full cursor control.
- Scrolling window adjustable (protected screen feature).
- Inverse/Normal video selection.
- · Versatile memory store/examine commands.
- · Read/Write cassette routines.
- · Dis-assembler.
- · Mini-assembler.
- Software simulated single-step and trace modes.
- · Breakpoint handling.
- · Register examine/modify.
- Input/Output device assignment.
- Hex add/subtract for relative branch calculations.
- · Editing on keyboard entry.
- · Direct calls to BASIC.
- Floating point package.
- Software simulated 16-bit processor.

GET ON THE LIST AND RECEIVE YOUR APPLE II BEFORE THE RUSH!

Send your advance order to:

Apple Computer 770 Welch Road Palo Alto, California 94304

Apple][Advance Order Information

THE OFFER

We expect a sizable backlog of orders almost immediately after APPLE-II is nationally announced in April. This advance offer is extended to allow you to order an Apple-II from the first production run, thus be guaranteed delivery by April 30, 1977. The terms of the advance order are as

- 1. All orders will be processed on a first-come, first-served basis regardless of quantity.
- 2. A deposit of one-third (1/3) of the total dollar amount ordered will be required and must accompany the order, the balance due on delivery.
- 3. All California residents must add 612% sales tax on retail orders.
- 4. Apple will pay all shipping (UPS) and order handling charges.
- 5. Delivery is guaranteed on or before April 30, 1977.

The only other items required to start using your APPLE-II are:

- An ASCII encoded keyboard.
- · A video monitor (or home TV with RF modulator).
- A power supply:
 - 5v @ 2A
 - 12v @ 1.5A
 - -5v @ .5A
 - 12v @ .5A

(These current requirements will supply a totally loaded Apple-II with 8 peripheral boards!)

· Case (optional).

All four items above will be available from Apple in April. Check with us for prices.

PRICES

APPLE-II board, completely assembled & tested with 4k bytes RAM

Additional RAM: each 4k bytes

\$600.00

each 16k bytes

\$125.00 \$600.00

APPLE-II prices with all memory options are:

	Column A	Column B	
Bytes RAM	Price	Price including 6½% sales tax California residents only	
4k	\$ 600.00	\$ 639.00	
8k	725.00	772.12	
12k	850.00	905.25	
16k	1,075.00	1,144.87	
20k	1,200.00	1,278.00	
24k	1,325.00	1,411.12	
32k	1,675.00	1,783.87	
36k	1,800.00	1,917.00	
48k	2,275.00	2,422.87	

Quantity	Memory		Price each*	Pric	e × Quantity	
	Apple-II(s) with	k RAM at	: \$ eac	h. Total	= \$	
2	Apple-II(s) with	k RAM at	: \$ eac	h. Total	= \$	
3	Apple-II(s) with	k RAM at	\$ eac	h. Total	= \$	
		4.	SUBTOTAL	\$		_
		5.	1/3 advance payn	nent \$		-
		6.	BALANCE DUE	\$		-
			(subtract item 5 fro	m		
	sidents must include 61 2°6		item 4) tail sales and should	use price:	s from column E	3 in the price list (which
already includ Orders from a	sidents must include 612°c e 612% sales tax). Il other states should use p	sales tax on ret	tail sales and should	·		3 in the price list (which
already includ Orders from a	e 61/2% sales tax). Il other states should use p	sales tax on ret	tail sales and should			
already includ Orders from ai Name Street	e 612% sales tax). Il other states should use p	o sales tax on ret	tail sales and should			
already includ Orders from al Name Street City	e $6^{1}2\%$ sales tax). Il other states should use p	o sales tax on ret	tail sales and should mn A.	te		Zip